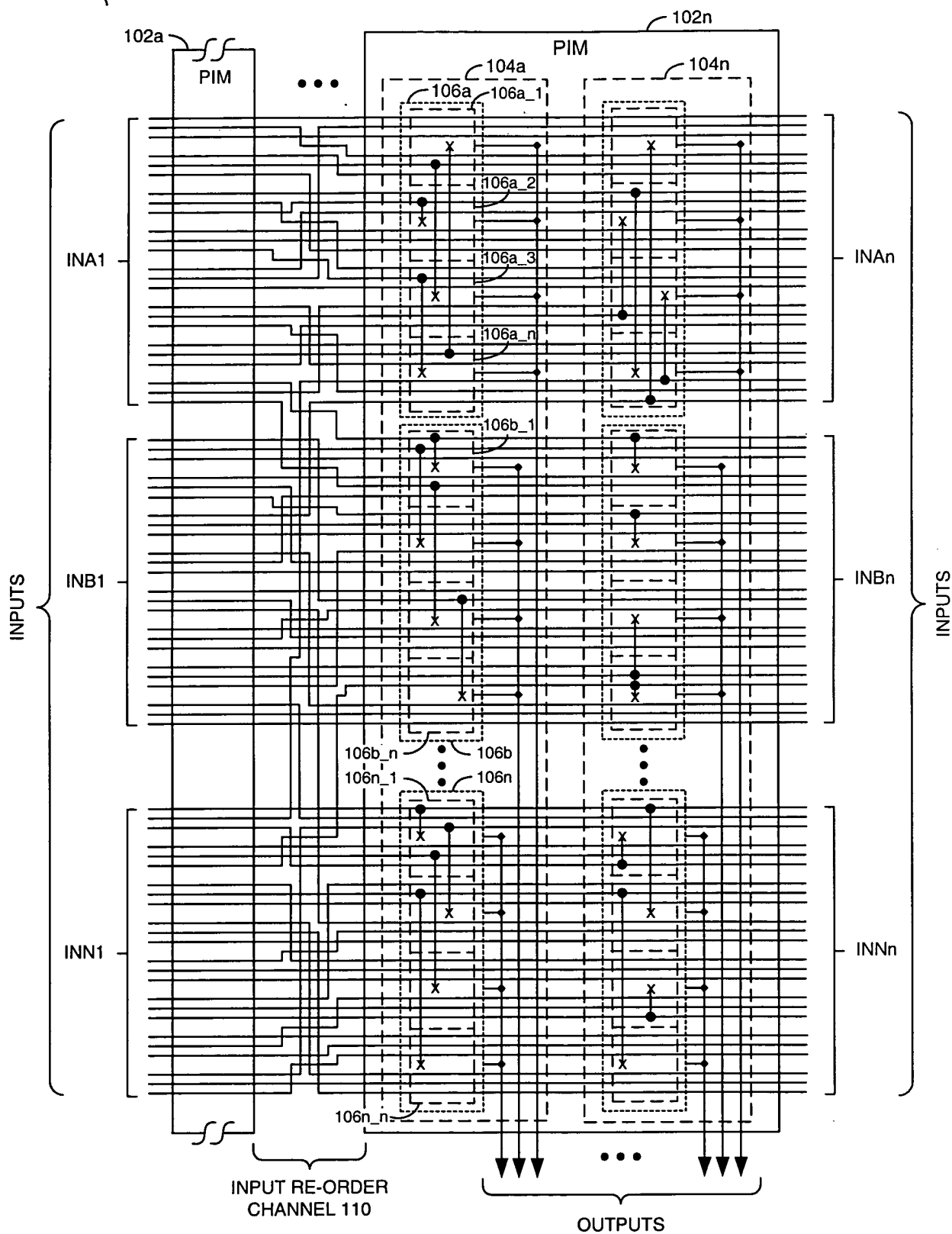


**FIG. 1**

**Applicant:** Brian P. Evans et al.  
**Serial No.:** 09/943,947  
**Filing Date:** August 31, 2001  
**For:** CONFIGURABLE MATRIX ARCHITECTURE  
**Attorney:** CPM  
**Attorney Docket No:** 0325.00504

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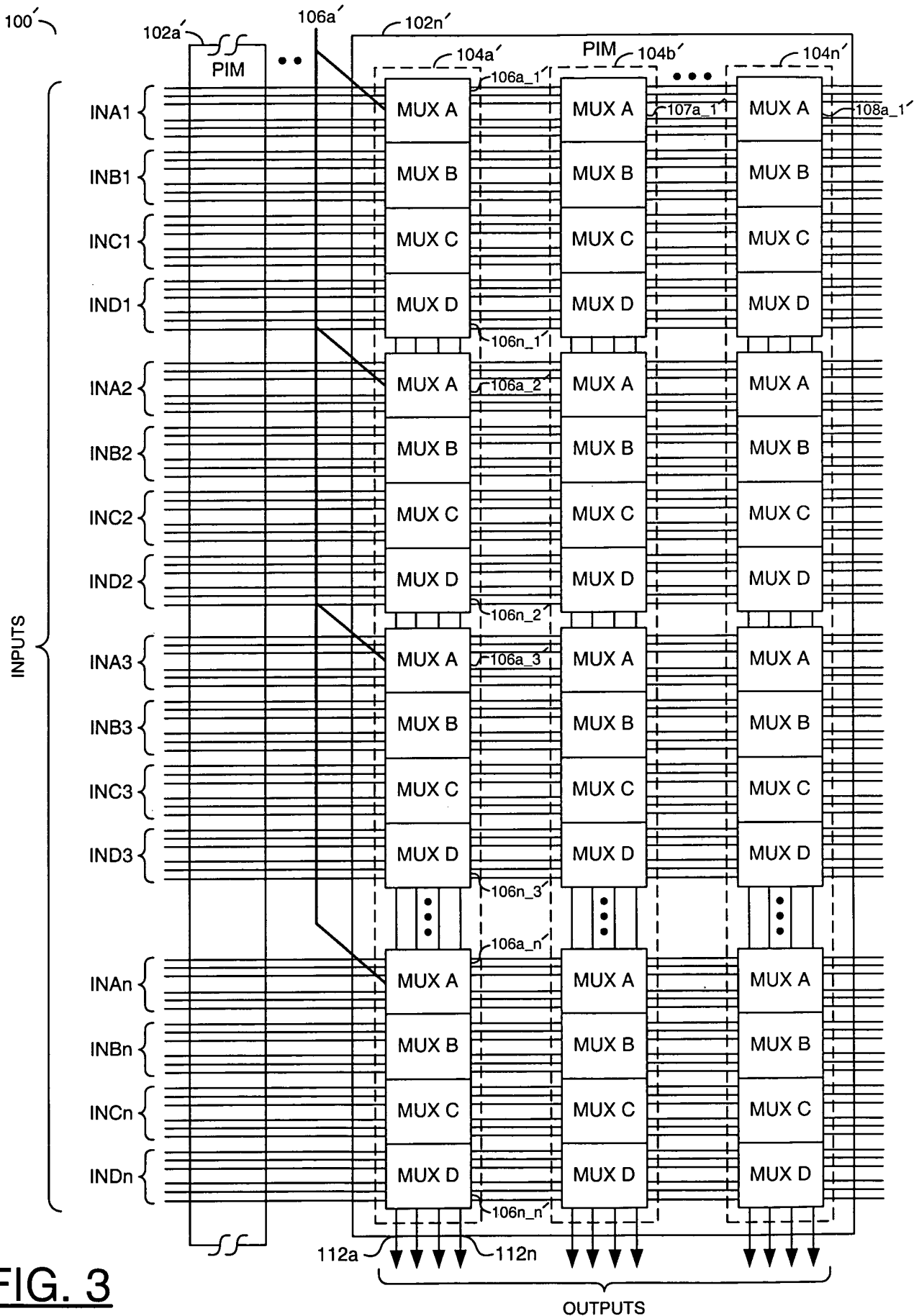


- CONNECTION TO INPUT SIGNAL
- x CONNECTION TO PIM BIT INPUT
- ◆ CONNECTION TO PIM BIT OUTPUT

**FIG. 2**

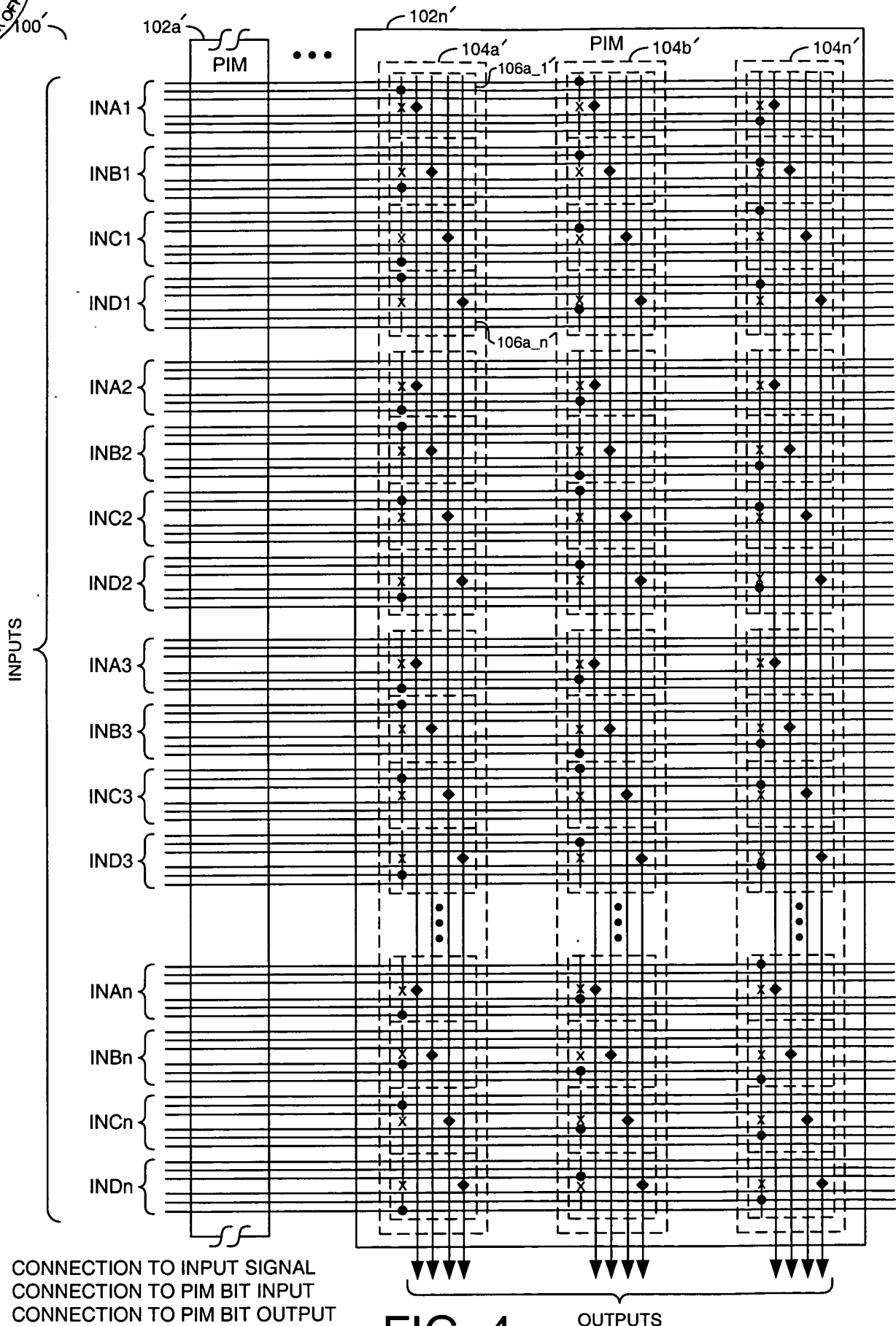
**Applicant:** Brian P. Evans et al.  
**Serial No.:** 09/943,947  
**Filing Date:** August 31, 2001  
**For:** CONFIGURABLE MATRIX ARCHITECTURE  
**Attorney:** CPM  
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**FIG. 4**

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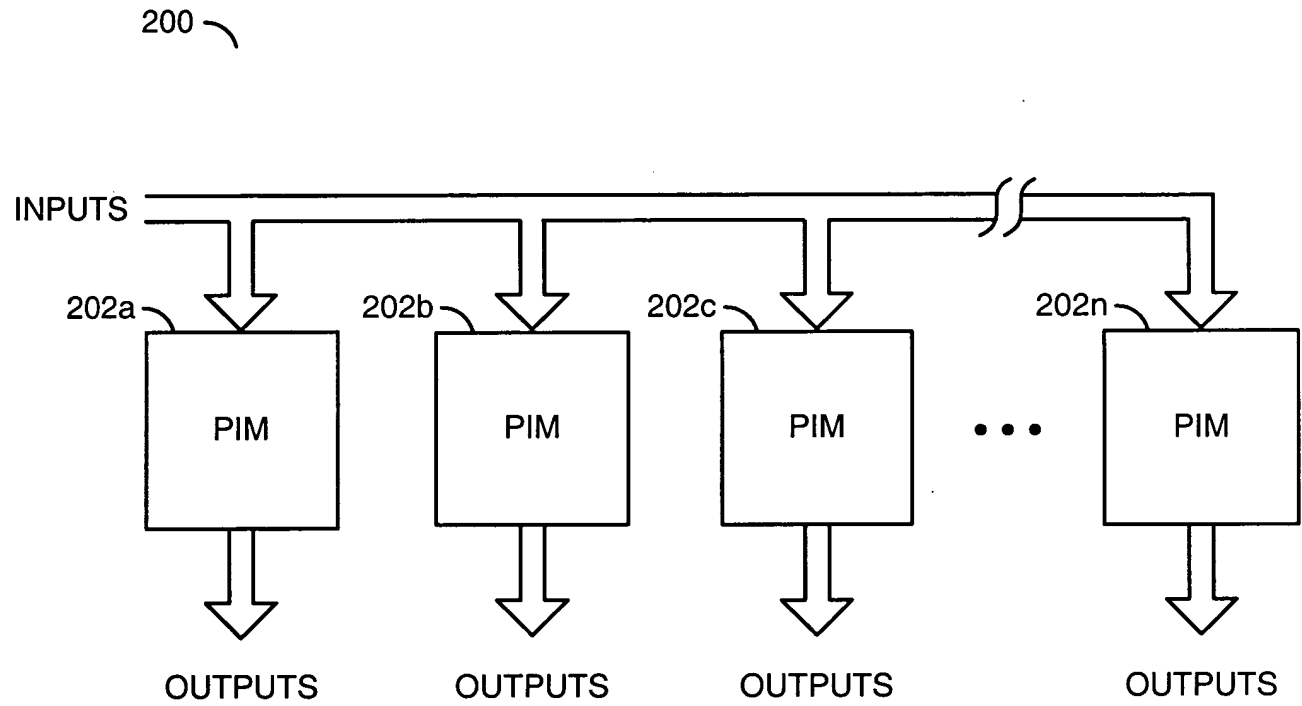


FIG. 5

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